

**Fig. 1**

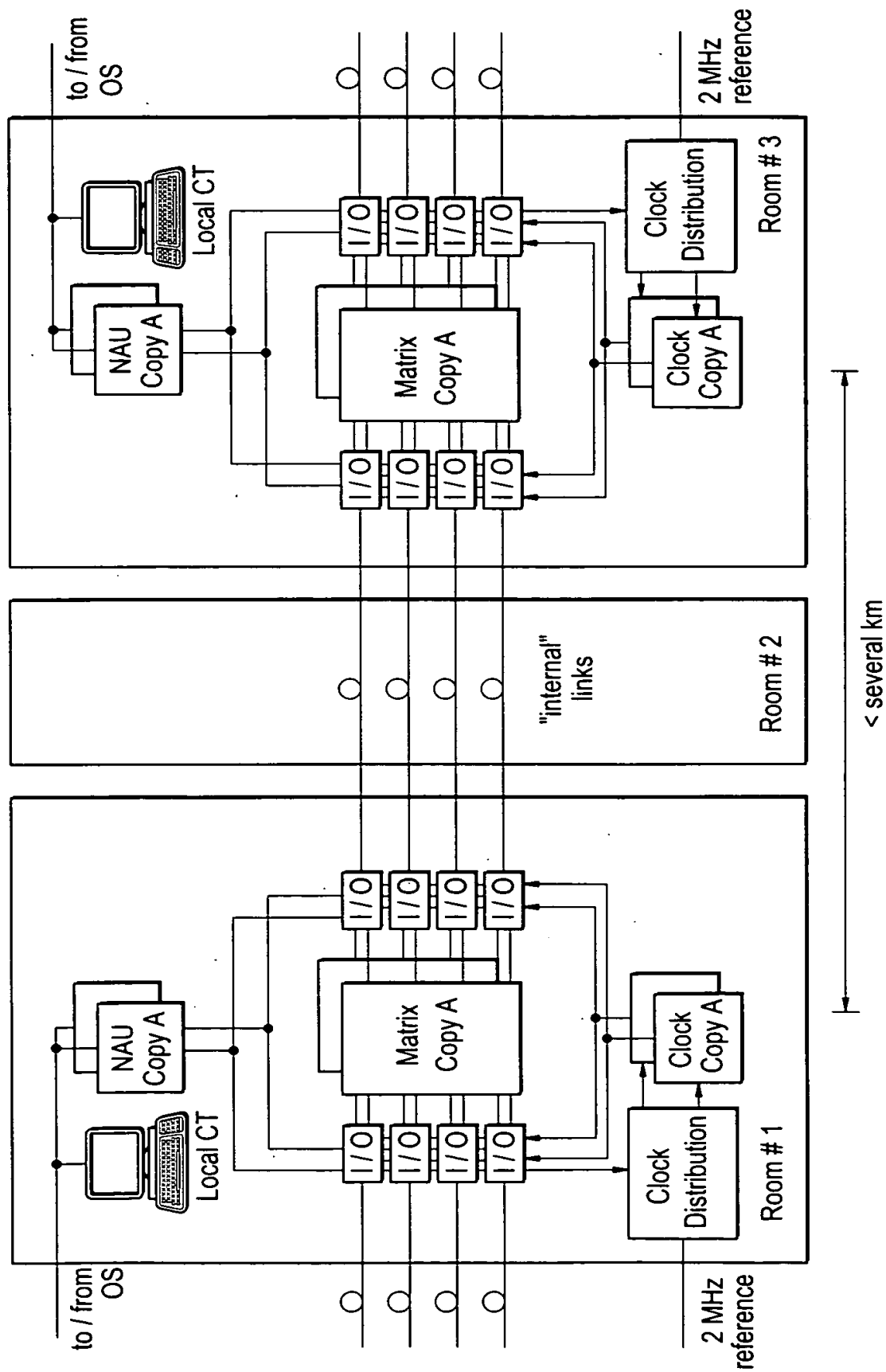


Fig. 2

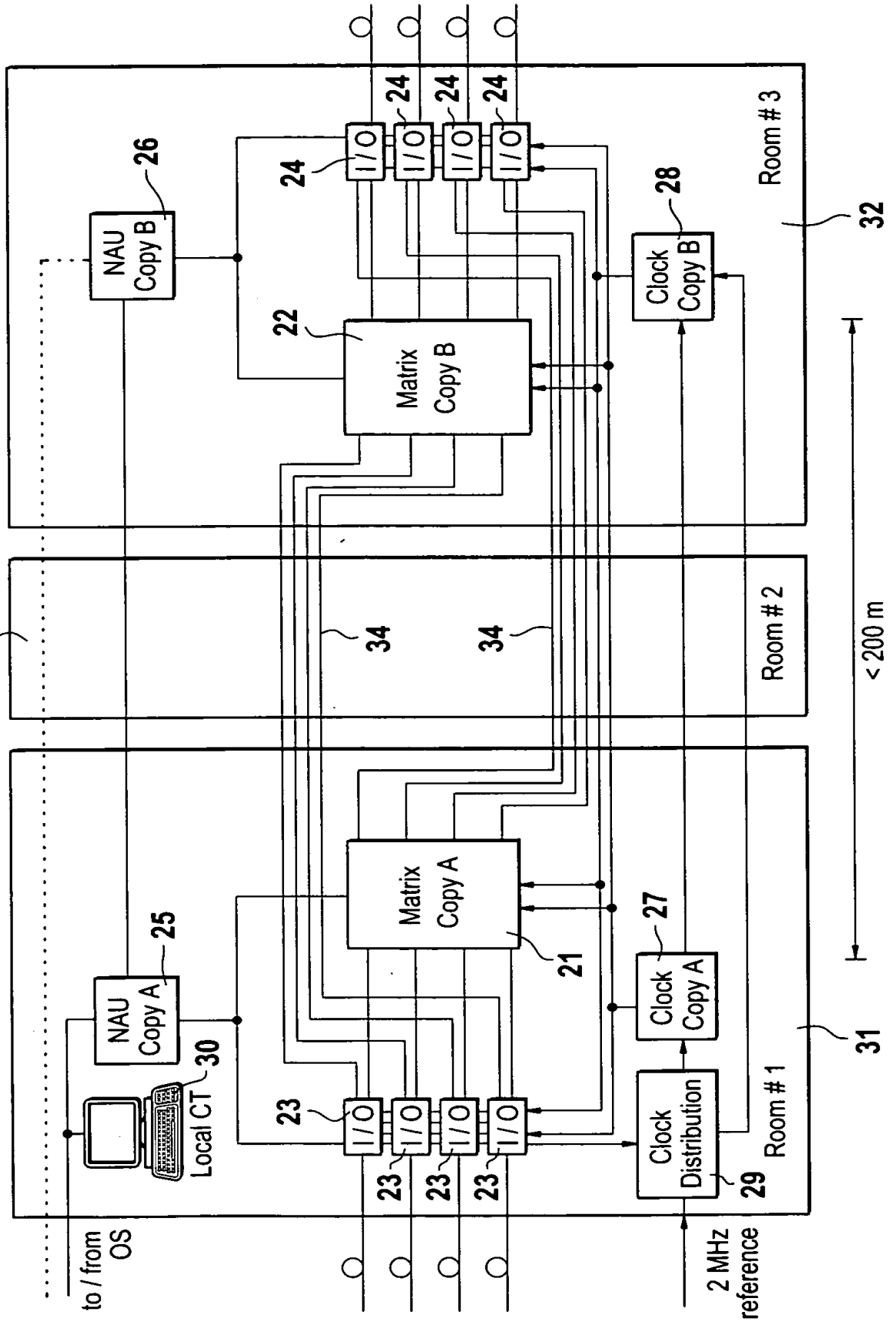


Fig. 3

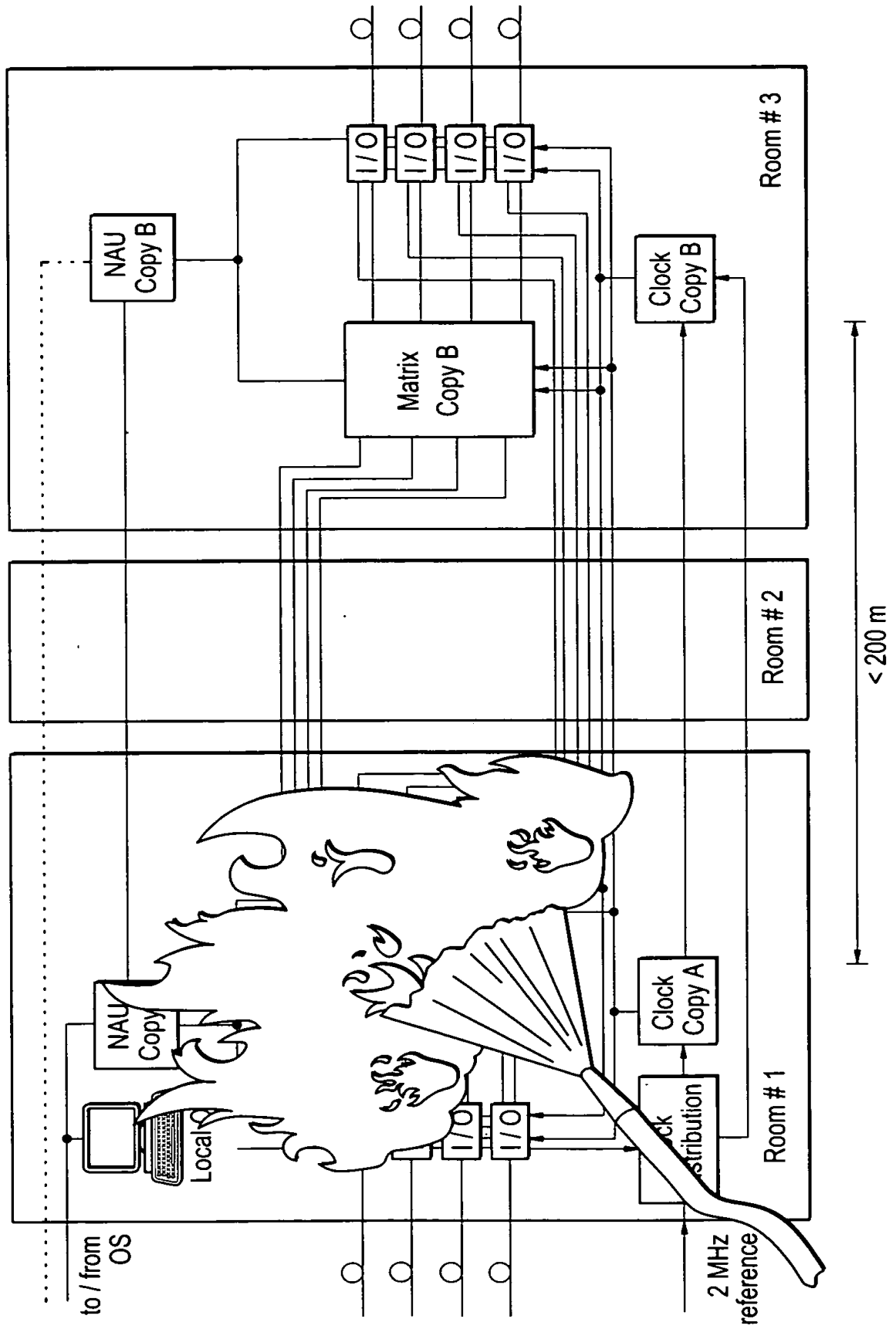


Fig. 4

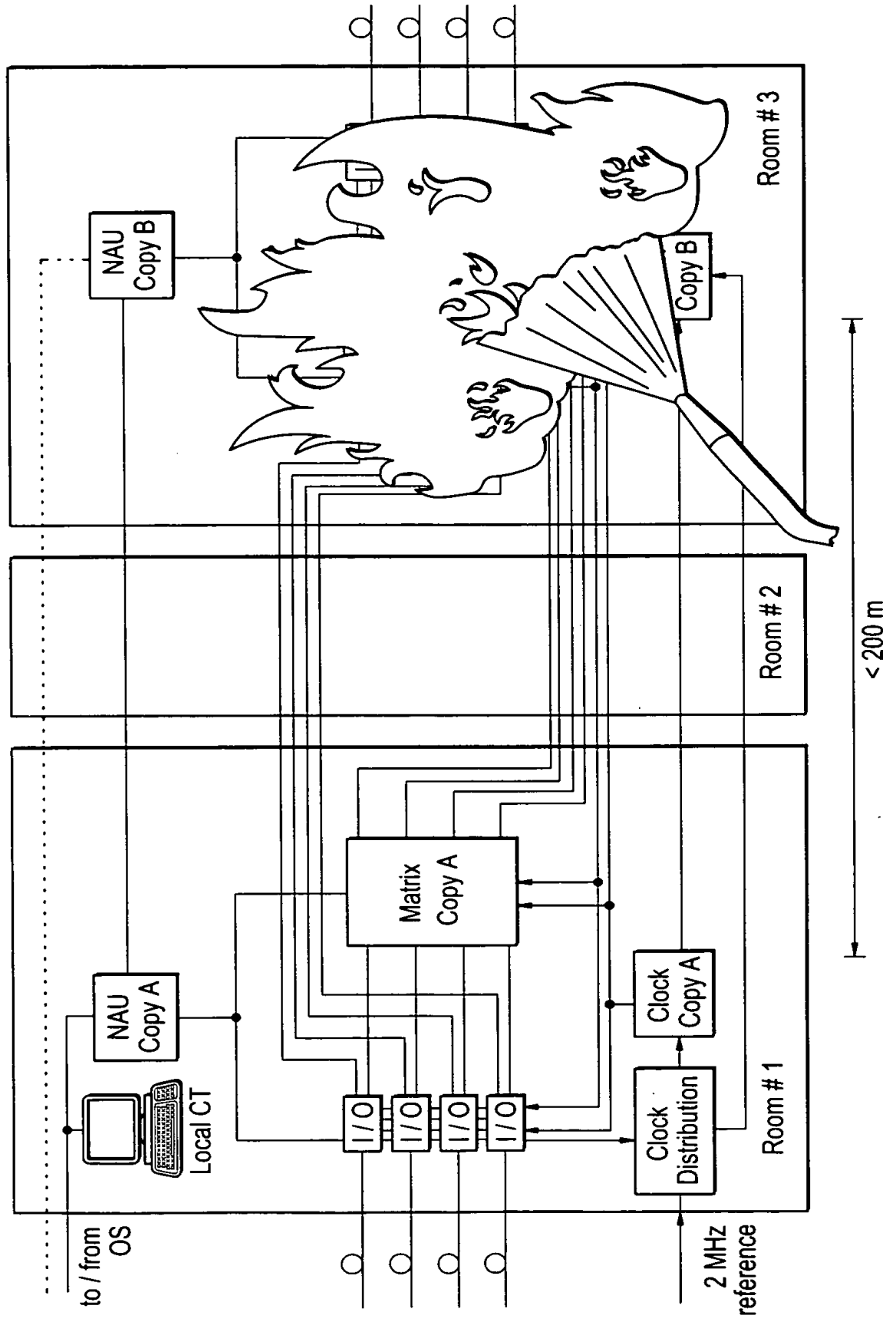


Figure 5 shows a schematic diagram of a system architecture across three rooms (Room #1, Room #2, and Room #3). The diagram illustrates the flow of data and control signals between various components, including a Local CT, NAU Copy A, Matrix Copy A, Clock Distribution, Clock Copy A, Matrix Copy B, NAU Copy B, and Clock Copy B. A 2 MHz reference signal is provided to the Clock Distribution unit in Room #1. A large starburst symbol in Room #2 indicates a central point of connection or a critical event. The total distance between Room #1 and Room #3 is indicated as less than 200 m.

Fig. 5

